

Leo Filippini

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Summary I am a PhD candidate focusing on charge recovery logic and low-power VLSI systems, with a strong background in analog IC design and layout in deep-submicron CMOS. I have cleanroom and tapeout experience and a sound understanding of transistor level design and device physics.

Education

- Present **PhD candidate**, *Drexel University*, Philadelphia (PA).
Electronics Engineering
- 2013 **Master Degree**, *University of Brescia*, Brescia (Italy), *summa cum laude*.
Electronics Engineering
- 2010 **Bachelor Degree**, *University of Brescia*, Brescia (Italy).
Information Engineering

Experience

- Present **Research Assistant**, *Drexel University*, Philadelphia, PA (USA).
My research is focused on low-power methodologies for VLSI circuits. In particular I am working on logic synthesis and power prediction for charge recovery logic systems, and how to apply charge recovery principles to analog circuits.
- Present **Teaching Assistant**, *Drexel University*, Philadelphia, PA (USA).
I have been the lab instructor for several undergraduate classes: Digital Electronics, Advanced Electronics I, Analog Electronics, and Electronic Devices. I conduct laboratory sessions, grade homeworks and lab reports, and I help students with the classes' final project.
- 2017 **Instructor**, *Drexel University*, Philadelphia, PA (USA).
I was the co-instructor for Advanced Electronics I, a class focusing on analog design for integrated circuits.
- 2017 **Senior Design Project Advising**, *Drexel University*, Philadelphia, PA (USA).
I, along with two faculty members, advised one of the senior design teams of academic year 16/17. During our weekly meetings, we discuss research ideas and their feasibility.
- 2016 **Undergraduate Mentoring**, *Drexel University*, Philadelphia, PA (USA).
Drexel University STAR initiative allows undergraduate students to spend their freshman summer doing research. I was part of the team that mentored several students, one of which closely worked with me and helped me in my research.
- 2013 **Intern**, *Imec Belgium*, Heverlee (Belgium).
During this internship, which is also my Master's thesis, I designed an integrated transimpedance amplifier for capacitive ultrasonic transducers (CMUT).

2010 **Intern**, *University of Brescia – Physics Department*, Brescia (Italy).

For four months I worked on my Bachelor's thesis: *Synthesis and integration of quantum dot semiconductors in third generation excitonic solar cells*. Along with my supervisors, we chemically synthesized different types of quantum-dots and realized many cells. I, in particular, took care of the substrate deposition and characterization, of the construction of the cells, and of their optical and electrical characterization. To do so, I used the following instruments: electronic load with 4-point probe, solar simulator, monochromator, lock-in amplifier (in order to measure cells' IPCE).

Honors & Awards

2017 Weggel Family Fellowship

2016 Joseph and Shirley Carleone Endowed Fellowship

2011 Winner of European *Lifelong Learning Program* scholarship

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