Dongen Zhou

Phone: 215-939-6807 Department of Electrical and Computer Engineering Drexel University, Bossone 405, 3141 Chestnut Street E-mail: dz78@drexel.edu Philadelphia, PA 19104-2816 Url: vlsi.ece.drexel.edu EDUCATION \diamond **M.S., Computer Engineering**, GPA: 3.2, (expected graduation 2018). Drexel University, Philadelphia, PA ♦ B.S., Computer Engineering, GPA: 3.1, 2016. Drexel University, Philadelphia, PA ♦ B.S., Electrical Engineering, GPA: 3.1, 2016. Drexel University, Philadelphia, PA ♦ Study Abroad, 2014 Technical University of Denmark (DTU), Lyngby, Denmark EXPERIENCE \diamond Teaching Assistant, (09/2016 – current) Drexel University, Philadelphia, PA, USA ECEP 480, Solar Power Engineering, Spring 2016-17, Senior Level Class ECE 200, Digital Logic Design, Fall & Winter 2016-2017, Sophomore Level Class - Created multiple laboratory tutorials and handouts for the digital logic class - Instructed laboratory & recitation classes, held office hours \diamond Consultant. (06/2016 – current) Shanghai Da Cheng Cold Chain Logistics Co., Ltd., Shanghai, China - Providing technological solutions for logistical management, focusing primarily on: - Microsoft Excel (CSV) based solutions - Enterprise Resource Planning (ERP) functions - Efficient team-building - Providing management strategies and suggestions for the founder and executive team ♦ Assistant Technical Consultant, (04/2014 – 08/2014) Dowa Advanced Materials (Shanghai) Co., Ltd., Shanghai, China - Assisted in updating ERP system throughout the corporation: - Analyzed and designed user portals and interfaces - Performed resource planning independently for various departments - Maintained database with the headquarters in Tokyo, Japan ♦ Assistant Data Analyst, (07/2013 – 08/2014) Bank of Shanghai, Shanghai, China - Managed commercial banking database for regional headquarters - Analyzed corporate banking data and provided assistance with Microsoft Excel (CSV) based solutions - Assisted to interview, analyze, and document corporate customers with the corporate banking team

HONORS AND & Dean's Fellowship, College of Engineering, Drexel University, 02/2016 - Current

- - ◊ Launch It! Scholarship, C. D. Close School of Entrepreneurship, Drexel University, 01/2014
 - ♦ Winner of Break the Chain Contest, IntegriChain Incorporated, 06/2015
 - Champion, Student for the Advancement of Global Entrepreneurship (SAGE) China, 06/2008

- Member of the Champion Team in SAGE China's Entrepreneurship Competition

- Acted as an accountant for a multi-year charity project, selling donated goods

- RESEARCH \diamond A Cyber-Physical Platform for Smart Power Exchange in a Photovoltaic Assisted Power Network - Master's Thesis Project, 01/2017 - current
 - Research in an advanced power exchange network using Programmable System on-Chip (PSoC) and Field Programmable Gate Array (FPGA)
 - Design High Level System for AC Power exchange using Digital Signals
 - Build mathematical models for analysis conducted on hypothetical user profiles
 - Autonomous Robot Control Using Vision Senior Design Project, 09/2015 06/2016
 - Researched a comprehensive computer program that moves a Sphero robot autonomously out of a maze by recognizing and solving the maze based on camera vision at a bird's eve view
 - Designed and implemented cross-platform software solutions in processing visual data using Python based Breadth First Search (BFS) for Autonomous Planning and Scheduling (APS or AI Planning)
 - ♦ Satellite Image Processing Freshman Design Project, 03/2013 06/2013
 - Analyzed satellite images for civilian infrastructures affected by Hurricane Sandy through designing and implementing a Matlab-based solution
 - ◊ Scale-Space Representation and Distance Transform Project, 01/2014 04/2014
 - Implemented different methods for the Matlab-based reconstruction algorithm
- LANGUAGE \diamond Mandarin Chinese (Fluent), Wu Chinese (Fluent), English (Fluent)
 - ◊ Japanese (Proficient), German (Beginner), Danish (Beginner)
- GRADUATE & Advanced Geometry (Cohomology), Dynamic Systems
- COURSEWORK & ASIC Design I & II, VLSI Design, Custom IC Design, Advanced Computer Network
 - ♦ High Performance Architecture, Parallel Computer Architecture
 - ◊ Digital Electronics, Advanced Electronics, Digital Systems Design
 - ◊ Engineering Management I & II, Marketing: Identifying Customer Needs
 - - ◊ Graduate Student Member, IEEE Drexel University, 09/2012 - current
 - ♦ Staff, Drexel Recreation Center Drexel University, 01/2015 - 09/2015
 - Member, Cherry Hill Ken-yu Kai (UPenn/Princeton Kendo Club) University of Pennsylvania, 09/2011 - 10/2016
 - SKILLS & C, C++, C#, Assembly, VHDL, Verilog, Pthread, OpenMP, CUDA, BASH
 - ◊ Python, Java, Perl, Apple Xcode, Matlab, Maple, R studio, Mathematica
 - ◊ Synopsys Design Compiler, IC Compiler, HSpice Cadence – RTL Compiler, Encounter, Virtuoso Suite
 - ◊ Mono Xamarin, LATEX, Xilinx Vivado, Cypress PSoC IDE, OrCAD Pspice Designer
 - ◊ Microsoft Office Suite Excel, Outlook, PowerPoint, Publisher, Word
- REFERENCES ◊ Dr. Baris Taskin
 Professor, Department of Electrical and Computer Engineering
 Drexel University, Philadelphia, PA
 E-mail: taskin@coe.drexel.edu
 ◊ Dr. Prawat Nagvajara
 Associate Professor, Department of Electrical and Computer Engine
 - Associate Professor, Department of Electrical and Computer Engineering Drexel University, Philadelphia, PA E-mail: nagvajara@coe.drexel.edu